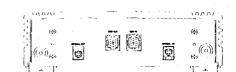


# Service Manual



UNIVERSAL DIGITAL PREAMP/EQUALIZER OPTICAL DIGITAL REFERENCE SYSTEM

# RS-P50

UC,EW

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## **SAFETY INFORMATION (UC MODEL)**

## **CAUTION**

This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely; you should not risk trying to do so and refer the repair to a qualified service technician.

### **WARNING**

Lead in solder used in this product is listed by the California Health and Welfare agency as a known reproductive toxicant which may cause birth defects or other reproductive harm (California Health & Safety Code, Section 25249.5). When servicing or handling circuit boards and other components which contain lead in solder, avoid unprotected skin contact with the solder. Also, when soldering do not inhale any smoke or fumes produced.

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FK OCT. 1993 Printed in Japan

## 1. SPECIFICATIONS

GENERAL
Power SourceDC 14.4V(10.8—15.6V allowable)
Grounding systemNegative type
Current consumption1A(without center speaker)
2.5A(rated power of center speaker)
Fuse4A
Dimensions202(W)×56(H)×260(D)mm
[8(W)×2-1/4(H)×10-1/4(D)in.]
Weight2.8kg(6.2lbs.)
•
DSP/PREAMP
Tone controls(parametric)
Bass frequency63Hz,100Hz,160Hz,250Hz
Treble frequency4kHz,6.3kHz,10kHz,16kHz
Level± 12dB
16-band graphic equalizer(front/rear)
Frequency20Hz-20kHz,2/3oct.
Level± 12dB
Network
SUBWOOFER
HPF frequency:20Hz—100Hz,1/3oct.
LPF frequency:25Hz-250Hz,1/3oct.
Level:+10dB24dB(0.5dB)
LOWHPF frequency:25Hz-250Hz,1/3oct.
LPF frequency:250Hz—10kHz,1/3oct.
Level:0dB24dB(0.5dB)
MIDHPF frequency:200Hz—10kHz,1/3oct.
LPF frequency:2kHz—20kHz,1/3oct.
Level:0dB24dB(0.5dB)
HIGHHPF frequency:1.6kHz20kHz,1/3oct.
LPF frequency:8kHz—20kHz,1/3oct.
Level:0dB24dB(0.5dB)
SlopePASS,-6,-12,-18,-24,-30,-36dB/oct.
(HPF of MID and HIGH dosen't have PASS mode)
PhaseNORMAL/REVERSE
Time alignment0—10msec.
NAC(Natural Acoustic Control)
Early reflectionInitial delay:2—22msec.
Liveness: ± 10 step
Room size: ± 10 step
Level:-20-+5dB
HF reflection: ±2 step
ReverberationInitial delay:30—120msec.
Level:-20—+10dB
Reverberation time:400—3,150msec.

HF reverberation: ±2 step Reverberation fader:0—10dB

Center speaker network
HPF frequency:100Hz10kHz,1/3oct.
Slope:-12dB/oct.
LPF frequency:630Hz—PASS,1/3oct.
Slope:-6dB/oct. or PASS
Level:0-24dB(0.5dB)
Time:0—10msec.
Position adjustmentTime:0—10msec.
Level:0-30dB
Sampling frequency44.1kHz
Digital inputOptical input
Digital outputOptical output
Digital output
DO CONTRACTOR AND A SELECTION AND ASSESSMENT AND A SERVICE
RCA OUTPUT(2V output, 15kΩ terminate)
•
Frequency response10Hz—20kHz(+0,-1dB)
Frequency response10Hz—20kHz(+0,-1dB) Max. output level/impedance2V/1kΩ
Frequency response10Hz—20kHz(+0,-1dB) Max. output level/impedance2V/1kΩ Distortion0.005%(1kHz,20kHz LPF)
Frequency response

These specifications were determined and presented in accordance with specification standards established by the Ad Hoc Committee of Car Stereo Manufacturers.

Continuous power output(EW)

15W(20Hz—20kHz, 1%,  $4\Omega$ )

The specifications and design are subject to change without prior notice.

Products purchased may differ from illustrations of

this manual.



# Service Manual

ORDER NO. CRT1788

UNIVERSAL DIGITAL PREAMP/EQUALIZER OPTICAL DIGITAL REFERENCE SYSTEM

RS-P50

EW8

● As to RS-P50/EW8, refer to CRT1544 (RS-P50/EW) because of the same contents.

PIONEER ELECTRONIC CORPORATION 4-1, Meguro 1-Chome, Meguro-ku, Tokyo 153, Japan PIONEER ELECTRONICS SERVICE INC. P.O.Box 1760, Long Beach, CA 90801-1760, U.S.A. PIONEER ELECTRONIC (EUROPE) N.V. Haven 1087, Keetberglaan 1, 9120 Melsele, Belgium PIONEER ELECTRONICS ASIACENTRE PTE. LTD. 501 Orchard Road, #10-00 Lane Crawford Place, Singapore 0923

## 2. DISASSEMBLY

## ●Heat Sink

- 1.Remove the six screws.
- 2.Remove the heat sink.

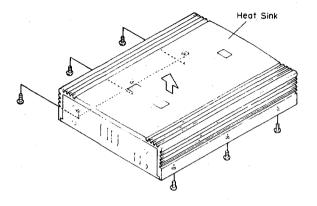


Fig.1

## **●**Panel

- 1.Remove the eight screws.
- 2.Remove the two panels.

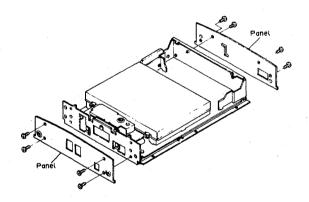


Fig.2

## **●**Chassis

- 1.Remove the five screws.
- 2.Unbend the claws at three locations until straight.
- 3.Remove the chassis.

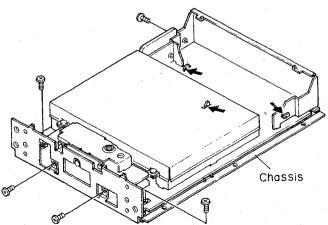


Fig.3

## Switch P.C.Board

- 1.Remove the connector.
- 2.Remove the screw.
- 3.Unbend the claws at three locations until straight.
- 4.Remove the switch P.C.Board.

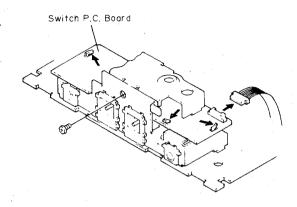


Fig.4

## ●DSP Unit

- 1.Remove the four connectors.
- 2.Remove the four screws.
- 3.Remove the DSP unit.

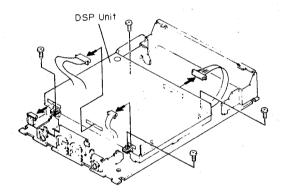


Fig.5

## Shield

- 1.Remove the case.
- 2.Unbend the claws at five locations until straight.
- 3.Remove the shield.

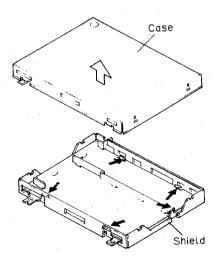


Fig.6

## 3. CIRCUIT DESCRIPTION

### System Initialization

The process for system initialization is as follows:

- 1. VDD 5 V is fed into the IC601 microcomputer.
- 2. BSENS and ASENSB pins are set L.
- 3. ROPW pin is set H and the ROMPOW 5 V is fed to the external ROM IC602.
- 4. The communication between the IC601 micro-computer and the external ROM IC602 begins.
- 5. Unless any error occurs in the communication between the microcomputer and the external ROM, IPPW pin is set H.
- 6. IP 5 V is fed into the IP-BUS circuit.
- 7. SYSPW is set H and the system is initialized.

### **Memory Protection Circuit**

When BACK UP is removed, Q954 is turned OFF and BSENS pin is set H. This makes the IC601 microcomputer find that BACK UP is removed. Then, oscillation of the microcomputer is stopped and the microcomputer enters the stop mode.

During the stop mode, current consumption is decreased.

In this state, the memory is protected by keeping VDD by means of C951. This memory protection circuit protects the memory contents for 3 or 4 days in the case where the car is inspected or the battery is replaced.

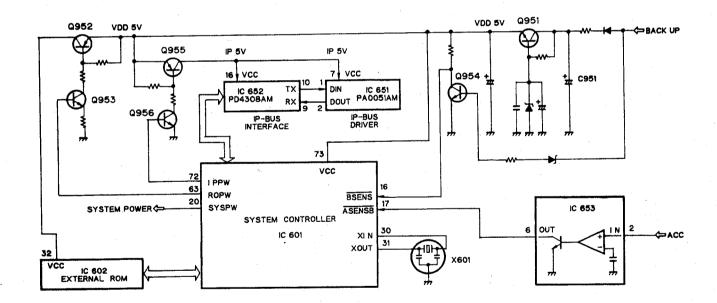


Fig.7

### Jitterless PLL

In generally used analog systems, it is impossible to prevent noise completely because a large amount of noise is generated from the vehicle. Therefore, an optical digital transmission system is adopted. This system is able to shut off electrical noise and does not have analog circuits that would be the cause of degradation of sound quality.

However, the optical fiber cable is not resistant to bending and the like, and thus jitters (irregular movement of clock pulses) are produced when the cable is installed in the interior of the vehicle. Because the jitter results in degradation of the sound quality, a jitterless PLL circuit that prevents jitters is needed.

The jitterless PLL circuit consists of the waveform shaping circuit (WF) and two systems of PLL circuits (see the figure).

- ① The data input from the receptacle involves slight jitters and irregular waveforms as a result of the way of installing the optical cable.
- ② The irregularity included in the input data is rectified by making the input signals pass through the waveform shaping circuit.
- ③ The waveform shaped data is input into DIR (IC601) and combined with one-sixth (64 fs) of the internal VCO (384 fs) to form PLL1. As a result, the internal

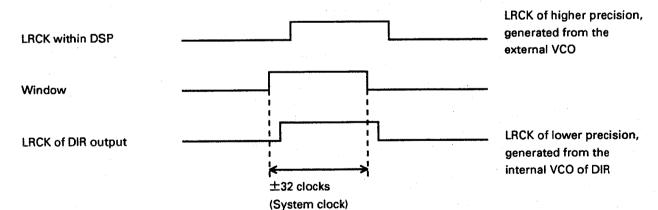
VCO is PLL-locked to the input signal by 384 times as large as the input data (fs = 44, 1 kHz).

Then, the output (384 fs) of the internal VCO is input into the DIR section to generate LRCK (fs) and BCK (64fs).

Should this internal VCO output be used as MCK (master clock) for DAC, the stability (precision) would be insufficient. Thus, the output is further combined with another external VCO (an oscillating device having the stability (precision) equivalent to a crystal oscillator) in order to form a loop of PLL2. The objective of the PLL2 is to get the master clock of higher precision as input into the DAC, so that the DAC can operate, giving its full capability.

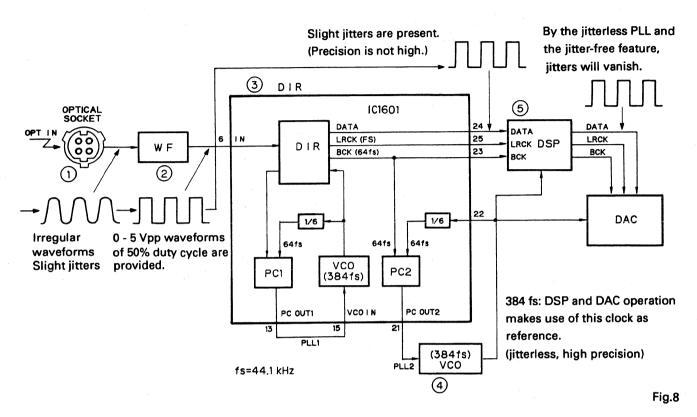
- A loop of PLL2 is formed by comparing the BCK (64
   fs) generated from the internal VCO (384 fs) with
   one-sixth of the external VCO. Sound quality is
   enhanced by using the output of PLL2 as master
   clock for DAC and DSP.
- ⑤ Since the LRCK and BCK which are outputs of DIR are generated from the internal VCO of DIR by means of dividing, use of them as they are brings on inadequate performance. In order to prevent this obstruction, the IC of DSP has a built-in feature called "jitter-free".

<Jitter-free feature>



If there are some jitters in external (input) LRCK, pulses can be output at the timing of LRCK of higher precision within DSP as long as they are inside the Window, and thus jitters will vanish.

In DSP and subsequent sections, jitter-free LRCK having higher precision, generated from the external VCO, is used.



## ●DSP System Diagram

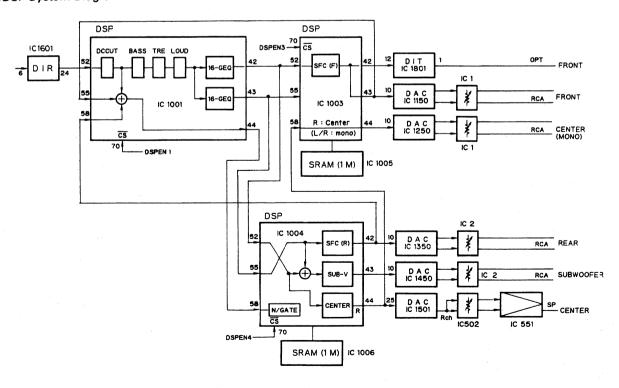


Fig.9

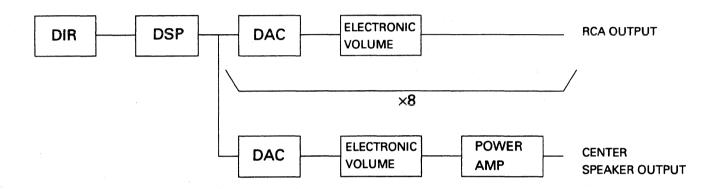
## RS-P50

## ●Analog Output Circuit

RS-P50 contains 9-channel DAC (8 channels for RCA output and one channel center SP) and an electronic variable resistor in order to make connection to the existing analog amp.

In DSP, as shown in the figure, digital data input from

DIR undergoes signal processing such as NAC (Natural Acoustic Control), 16-band GEQ, etc. The processed digital data is input into DAC and converted into analog signals. Then, the signals can be controlled by the electronic variable resistor. 1-bit DAC that is subject to less zero-cross distortion is used.



2 | 3 | 4 | 5 | 6 | **RS-P50** 

Clock System Diagram

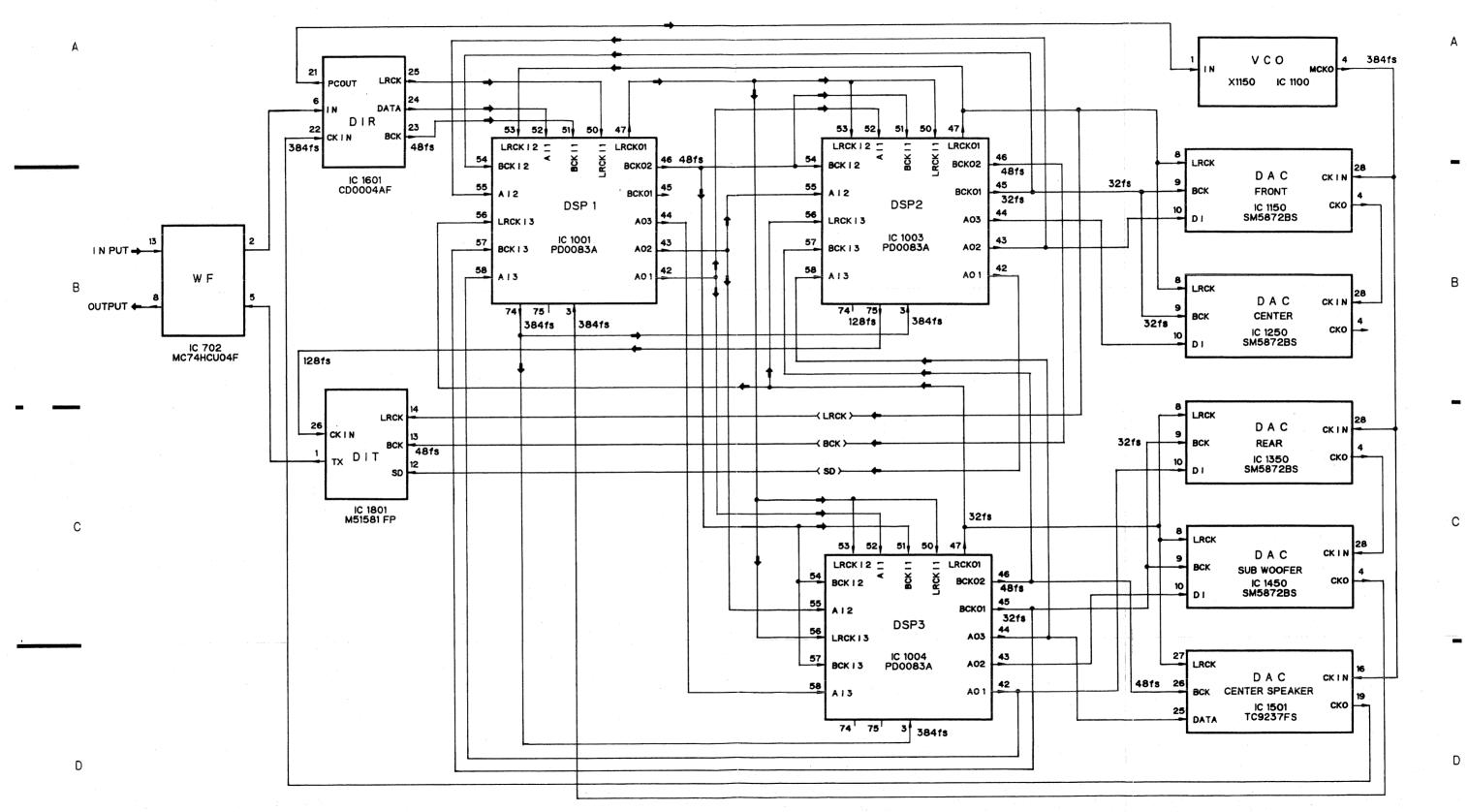
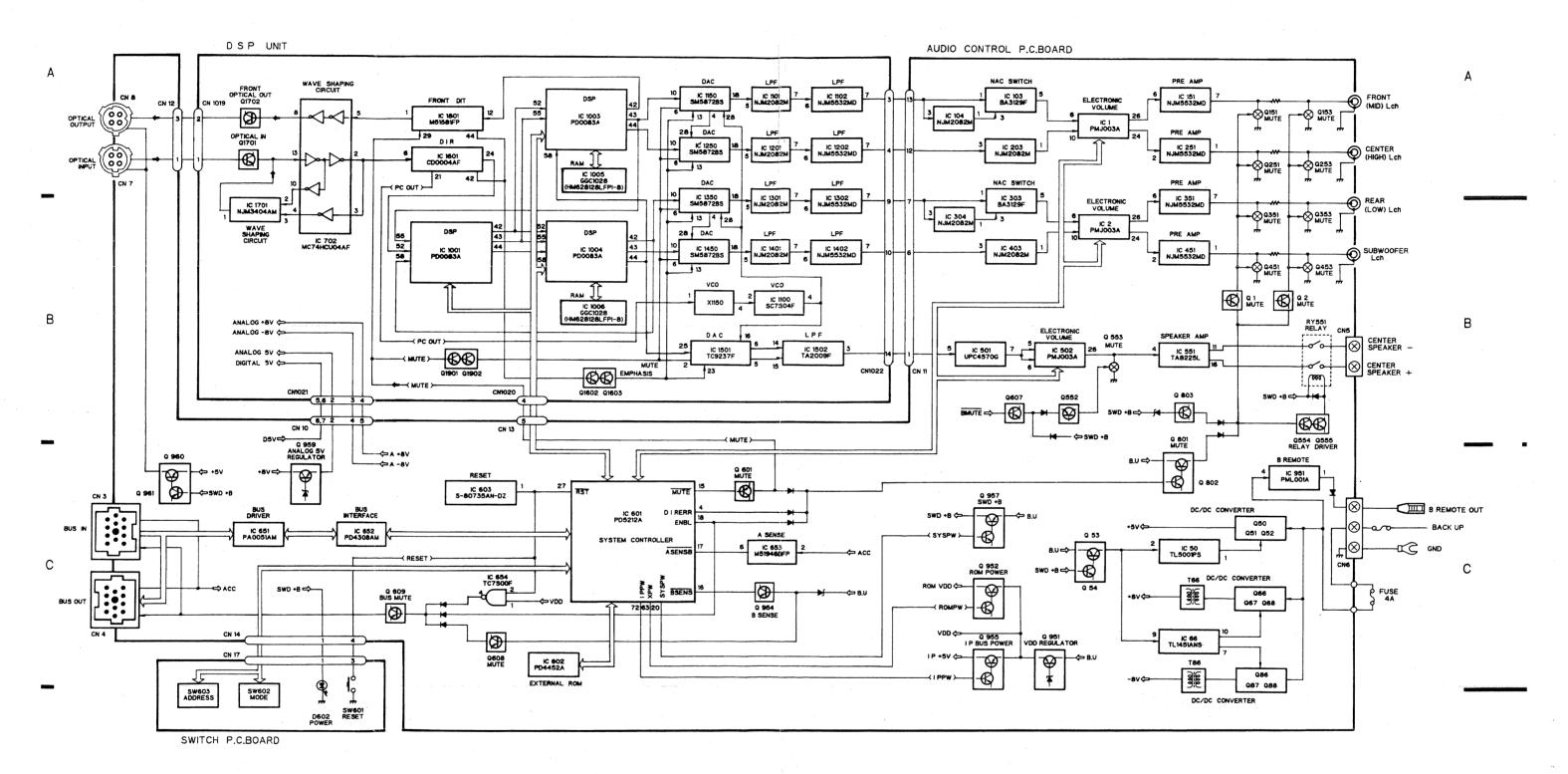


Fig.10

1

●Block Diagram



D

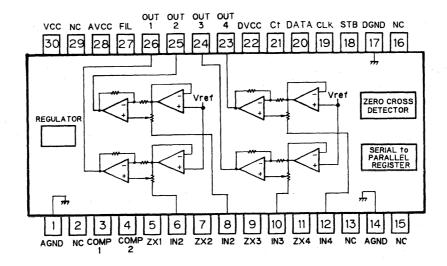
Fig.11

11 1 1 2 3 4 5 6

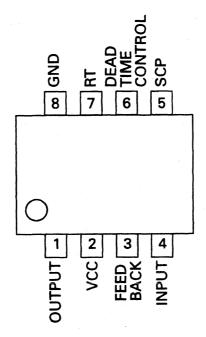
## **●**ICs

## **Audio Control Unit**

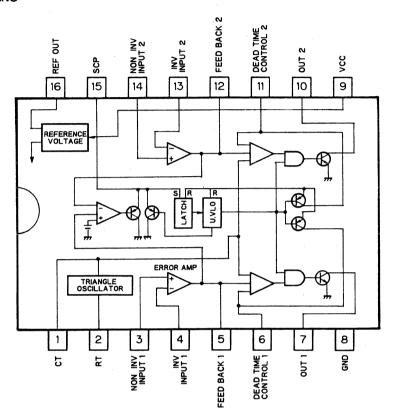
## IC1,2,502:PMJ003A



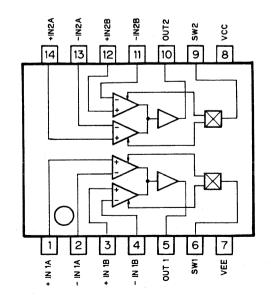
## IC50:TL5001PS



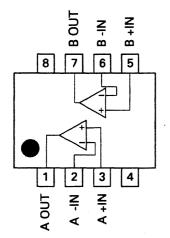
## IC66:TL1451ANS



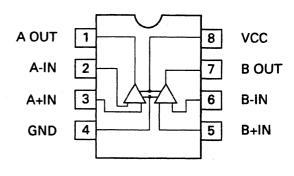
## IC103,303:BA3129F



IC104,203,304,403:NJM2082M DSP Unit IC1101,1201,1301,1401:NJM2082M

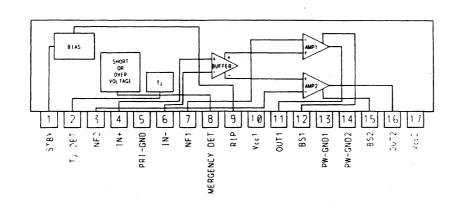


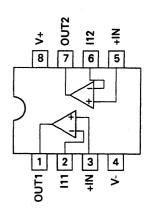
IC151,251,351,451:NJM5532MD DSP Unit IC1102,1202,1302,1402:NJM5532MD



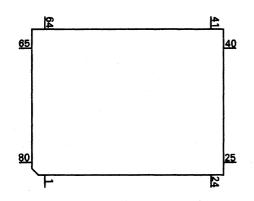
IC501:UPC4570G

IC551:TA8225L





## \*IC601:PD5212A



IC's marked by\* are MOS type.

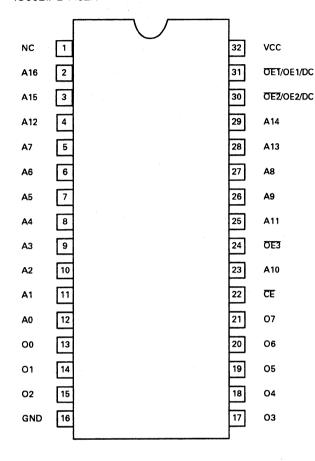
Be careful in handing them because they are very liable to be damaged by electrostatic induction,

## RS-P50

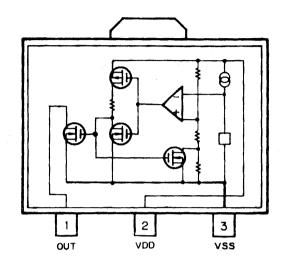
Pin 1 2	Pin Name	1/0	Output	Function
			Format	i unoson
2	DSPEN3	0	С	DSP enable
	NC			Not used
3	DSPEN1	0	С	DSP enable
4	DIRERR	ı	С	DIR error detector
5	DIRDA	ı	С	Audio/Digital switch
6	DIRFS	1	С	Frequency select terminal bit
7	TESTIN	ı		Test program mode input
8	DSPRDY	ı	С	Microcomputer I/F reception enable input
9	IPSCK	0	N	IP-BUS serial clock
10	IPOUT	0	N	IP-BUS serial data output
11	IPIN	I	С	IP-BUS serial data input
12	DSPRST	0	С	DSP reset control
13	DSPAD	0	С	DSP data/address switch
14	XA16	0	С	External ROM address output
15	MUTE	0	С	System mute
16	BSENS	I		Back up power sense input
17	ASENSB	1	С	Acc sense input
18	ENBL	1		Test program enable input
19	HPFP	0	С	High pass filter for switch ON/OFF
20	SYSPW	0	С	System power supply control output
21	DSPCK	0	С	DSP serial clock output
22	DSPOUT	0	С	DSP serial data output
23	DSPIN	1	С	DSP serial data input
24	XA15	0	С	External ROM address output
25	IPIRQ	1		Interrupt input from IP BUS interface IC
26	CNVSS	1		GND
27	RST	ı		Reset signal input terminal
28	SWST	0	С	Switch strobe
29	VCK3	0	С	Clock output of electronic volume 3
30	XIN	1		Crystal oscillating element connection pin
31	XOUT	0		Crystal oscillating element connection pin
32	VSS			GND
33-40	XDT7-0	ı	С	External ROM data input
41	XCE	0	С	Chip enable output for external IC
42-56	XA14-0	0	С	External ROM address output
57	XRD	0	С	External ROM read signal output
58-61	NC			Not used
62	ONW	1	С	Read cycle extension signal input
63	XPW	0	С	External ROM power
64	VCK2	0	С	Clock output electronic volume 2
65	VCK1	0	С	Clock output for electronic volume
66	VST	0	С	Strobe pulse output for electronic volume
67	VDT	0	С	Data output for electronic volume
68	IPCD	0	С	Command/data output for IP BUS interface IC
69	IPRW	0	С	Read / write output for IP BUS interface IC
70	IPCS	0	С	Chip select output for IP BUS interface IC
71	IPRST	0	С	Reset output for IP BUS interface IC
72	IPPW	0	С	Power supply control output for IP BUS interface IC
73	VCC			5V
74	VREF	0	С	OV
75	AVSS			GND
76-79	SWDT3-0	T	С	Switch data input
80	DSPEN4	0	С	DSP enable

Output Format	Meaning
С	CMOS
N	N channel open drain

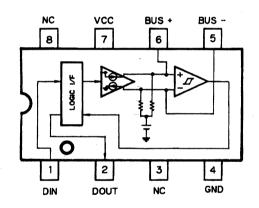
## \*IC602:PD4452A



\*IC603:S-80735AN-DZ



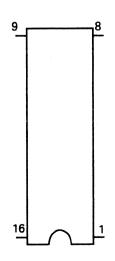
IC651:PA0051AM



A0-A16 : Address CE : Chip enable OE1-OE3 : Output enable O0-O7 : Output

DC NC : Don't care : No connection

## IC652:PD4308AM



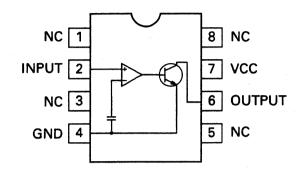
●Pin Functions(PD4308AM)

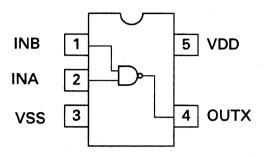
unctions(FD-500)	-141/	
Pin Nam	ne I/O	Function and Operation
IPSCK	1/0	Clock input/output
IPSI	T I	Data input
IPSO	0	Data output
IPIRQ	I	Interrupt input
IPRW	0	Read / write output
X1		Crystal oscillator connection pin
X0		Crystal oscillator connection pin
GND		GND
RX	Ī	Data input
TX	0	Data output
NC		Not used
IPCD	0	Command/data output
IPCS	0	Chip select output
IPRST	0	Reset output
16 VDD		Power supply
	Pin Nam IPSCK IPSI IPSO IPIRQ IPRW X1 X0 GND RX TX NC IPCD IPCS IPRST	IPSCK

## RS-P50

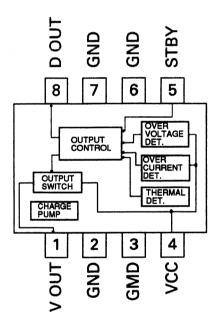
## IC653:M51946BFP

## \*IC654:TC7S00F



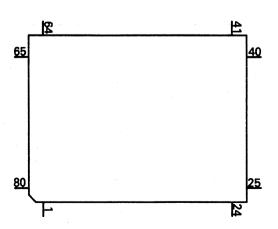


IC951:PML001A



## ●DSP Unit

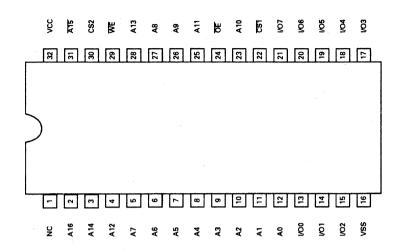
\*IC1001,1003,1004:PD0083A

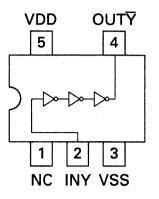


Pin Function	ons(PD0083A)		
Pin	Pin Name	1/0	Function
1	XIN	ı	Crystal oscillating element connection pin
2	XOUT	0	Crystal oscillating element connection pin
3	EXCK	ı	External clock input
4	VDD1		Power supply
5	WE0	0	Write enable pin of external RAM
6	ŌĒ0	0	Output enable pin of external RAM
7	WE1	0	Write enable pin of external RAM
8	ŌE1	0	Output enable pin of external RAM
9	CE	0	Chip enable pin of external RAM
10	CE	0	Chip enable pin of external RAM
11-18	RAMIO0-7	1/0	Data input output pin of external RAM
19	VSS1	***	GND
20-25	A16-11	0	Address output pin of external RAM
26	VDD2		Power supply
27-37	A10-0	0	Address output pin external RAM
38-41	TP1-4	<u> </u>	Test mode input pin
42-44	AOUT1-3	0	Lch,Rch audio serial data output pin
45	BCKOUT1	0	Bit clock of 32fs output pin
46	BCKOUT2	0	Bit clock of 48fs output pin
47	LRCKOUT1	0	LR clock output pin
48	LRCKOUT2	0	LR clock output pin
49	VSS2		GND
50	LRCKIN1		LR clock input pin1
51	BCKIN1		Bit clock input pin1
52	AIN1		Lch,Rch audio serial data input pin1
53	LRCKIN2	ı	LR clock input pin2
54	BCKIN2	<u> </u>	Bit clock input pin
55	AIN2	<u> </u>	Lch,Rch audio serial data input pin2
56	LRCKIN3		LR clock input pin3
57	BCKIN3	<u> </u>	Bit clock input pin3
58	AIN3	1	Lch,Rch audio serial data input pin3
59	VSS3		GND
60-64	TESTO0-4	0	Test output pin
65	DRDY	0	Microcomputer I/F reception enable output pin
66	TDATA	0	Microcomputer I/F transmission data output pin
67	RDATA	1	Microcomputer I/F reception data input pin
68	SCK		Clock input terminal serial data input
69	A/D		Address data discrimination input pin
70	CS		External RAM chip select
71	RESET		Reset input
72	XSEL	1	Frequency/external switch pin
73	VDD3		Power supply
74	384FS	0	Master clock output pin
75	128FS	0	128fs clock output pin for A/D converter
76	64FS	0	64fs clock output pin for A/D converter
77	32FS	0	32fs clock output pin for A/D converter
78	2FS	0	2fs clock output pin for A/D converter
79	FS	0	Fs clock output pin A/D converter
80	VSS4		GND
			· · · · · · · · · · · · · · · · · · ·

IC1005,1006:GGC1028(HM628128LFPI-8)

\*IC1100:SC7S04F



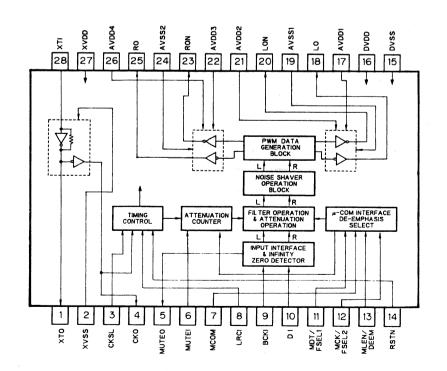


Functions(GGC1028)

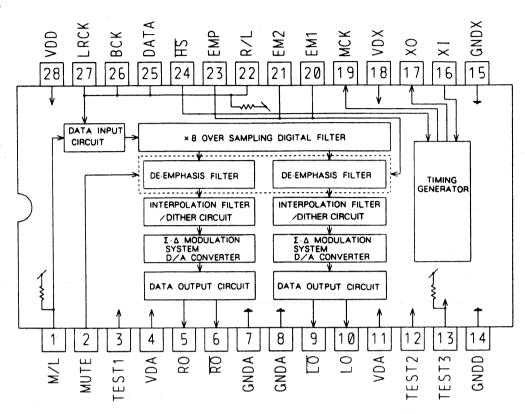
v	runctions(G	JC 1028)					
	CS1	CS2	ŌĒ	WE	Mode	1/0	Note
	Н	*	*	*	Not select	High-z	
	*	L	*	*	Not select	High-z	
	L	H	Н	Н	Read	High-z	Output disable
	L	Н	L	Н	Read	Dout	Read Cycle1-3
	L	Н	Н	L	Write	Din	Write Cycle1
	L	Н	L	L	Write	Din	Write Cycle2

<sup>\*:</sup>H or L

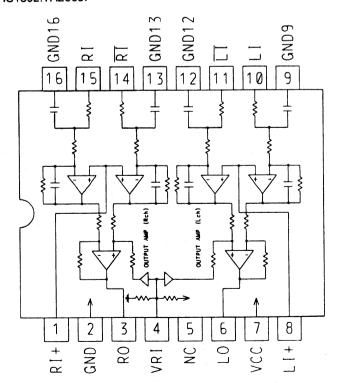
\*IC1150,1250,1350,1450:SM5872BS



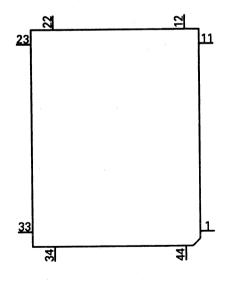
IC1501:TC9237F



IC1502:TA2009F



\*IC1601:CD0004AF



● Pin Functions(CD0004AF)

	tions(CD0004A		
Pin No.	Pin Name	1/0	Function and Operation
1	RESET	ı	Power ON/RESET input. Reset with "L"
2	16/24	1	Input format selecting terminal. provided with a pull-up resistor
3	Ā/M	<u> </u>	Input format selecting terminal. provided with a pull-up resistor
4,5	S1-2	1	Input selecting terminal
6-9	IN1-4		Data input terminal
10,11	TEST 1-2		Terminal for testing. Normally "H" or open. Provided with a pull-up resistor
12	PCVS		Input for setting self-propelling frequency for VCO
13	PCOUT1		Phase comparator output 1
14	R		Connecting terminal for VCO adjusting resistor
15	VCOIN		Control voltage input for VCO
16	VDD1		Power terminal for VCO line
17	VCOOUT	-	VCO output (384 fs)
18	VSS1		Grand terminal for VCO line
19,20	CA,CB		Connecting terminal for VCO adjusting capacity
21	PCOUT2		Phase comparator output 2
22	SIGIN	ı	Input terminal for external VCO
23	BCK	0	Demodulated data bit clock output (64 fs)
24	DATA	0	Demodulated audio data output
25	LRCK	0	Demodulated data LR clock output. L channel with "H"
26,27	OMODE0-1	0	Data output format selecting terminal
28	VSS		Grand terminal for logic lin
29	BLOCK	0	Block start output terminal
30	UBIT	0	User data output terminal
31	CBIT	0	Channel status output terminal
32	VBIT	0	Validity output terminal
33	VDD		Power terminal for logic line
34	CS		Chip select input terminal. Selecting state with "L"
35	SDATA	0	Serial data output terminal
36	SCK		Serial clock input terminal
37	COPY	0	Copy prohibit information output terminal
38	A/D	0	Audio/digital data information output terminal
39	DAT	0	DAT information output terminal
40,41	FS0-1	0	Sampling frequency information output terminal
42	EMPH	0	Emphasis information output terminal
43	ERR	0	Data reading error output terminal. Error with "H"
44	VCOINH		Input terminal for stop of oscillation of internal VCO. Stop with "H"
44	VCOINT		input terminal for stop of oscillation of internal VCO. Stop with "H"

## IC1701:NJM3404AM

VCC BOUT B-IN B+IN

B

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6

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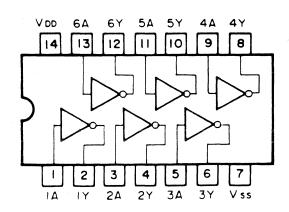
A +IN

A OUT A -IN

4

VEE

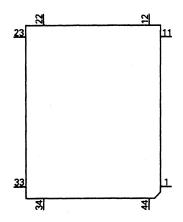
IC1702:MC74HCU04F



### ●Pin Functions(M51581FP

Pin Func	tions(M51581FP	1	
Pin	Pin Name	1/0	Function
1	TX	0	Digital audio interface format output
2	RESET	1.	Reset input
3	RX1		Digital audio data input 1
4	NFR	0	RX1 level converter output
5	RX2		Digital audio data input 2
6	RXSEL		RX select input
7,8	PD1,PD2	0	Phase comparative output for charge pump VCO
9	UNLOCK	0	Unlock detect output
10	RXCKI		VCO clock input
11	RXCKO	0	VCO clock output
12	SDI	1	Serial audio data input
13	BCK	1/0	Digital audio bit clock input/output
14	LRCK	1/0	Audio data word select input/output
15	SDO	0	Serial audio data output
16	ADSDI		A/D converter serial audio data input
17	VSS		GND
18	ADSEL	1	Serial data audio source select input
19	FLAGI		Error flag input
20	FLAGO	0	Error flag output
21	WCK	0	Word clock output
22	ASL		Audio data sampling length select input "H":24 bits "L":16 bits
23	IIS		Audio data format select input
24	MSBF	1	MSB select input
25	LRCKPOL		LRCK pole select input "H":Lch "L":Rch
26	MSTCK	1/0	Master clock input/output
27	CKSEL	1	Master clock frequency select input
28	REFCK	1	Reference clock input for sampling frequency accurate check
29	CKACO	0	Sampling frequency accurate check output
30	MUTE	1	Mute control input
31,32	MODE0-1	1	Mode select input
33	IN/out		Transmission reception select input
34,35	CAT0,1	1/0	Category information input/output
36	TXOE	T	TX output enable input
37	FSINSEL	1	fs information select input
38	VDD		Power supply
39	VSS		GND
40	TYPE	1/0	Type information input/output
41,42	FS0-FS1	1/0	fs information input/output
43	COPY	I/O	Copy information input/output
44	EMP	1/0	Emphasis information input/output

## \*IC1801:M51581FP



## 4. DC/DC CONVERTER CIRCUIT DIAGRAM

Α

DC/DC CONVERTER +84 **\***0\* 20≘ 3 - 2 20 3 C61 R881 .880 363

В

C

Fig.12

D

С

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## **5. SCHEMATIC CIRCUIT DIAGRAM**

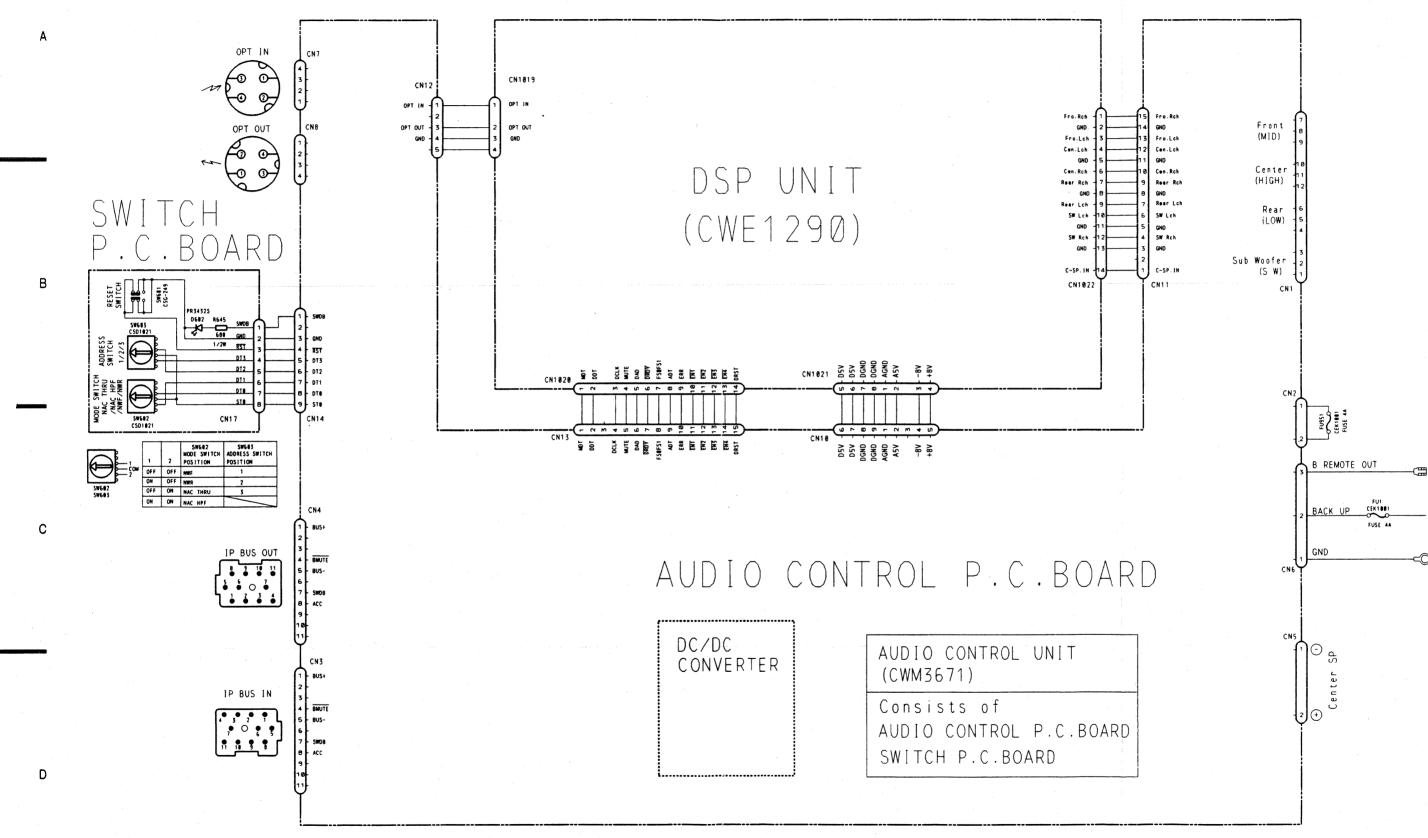
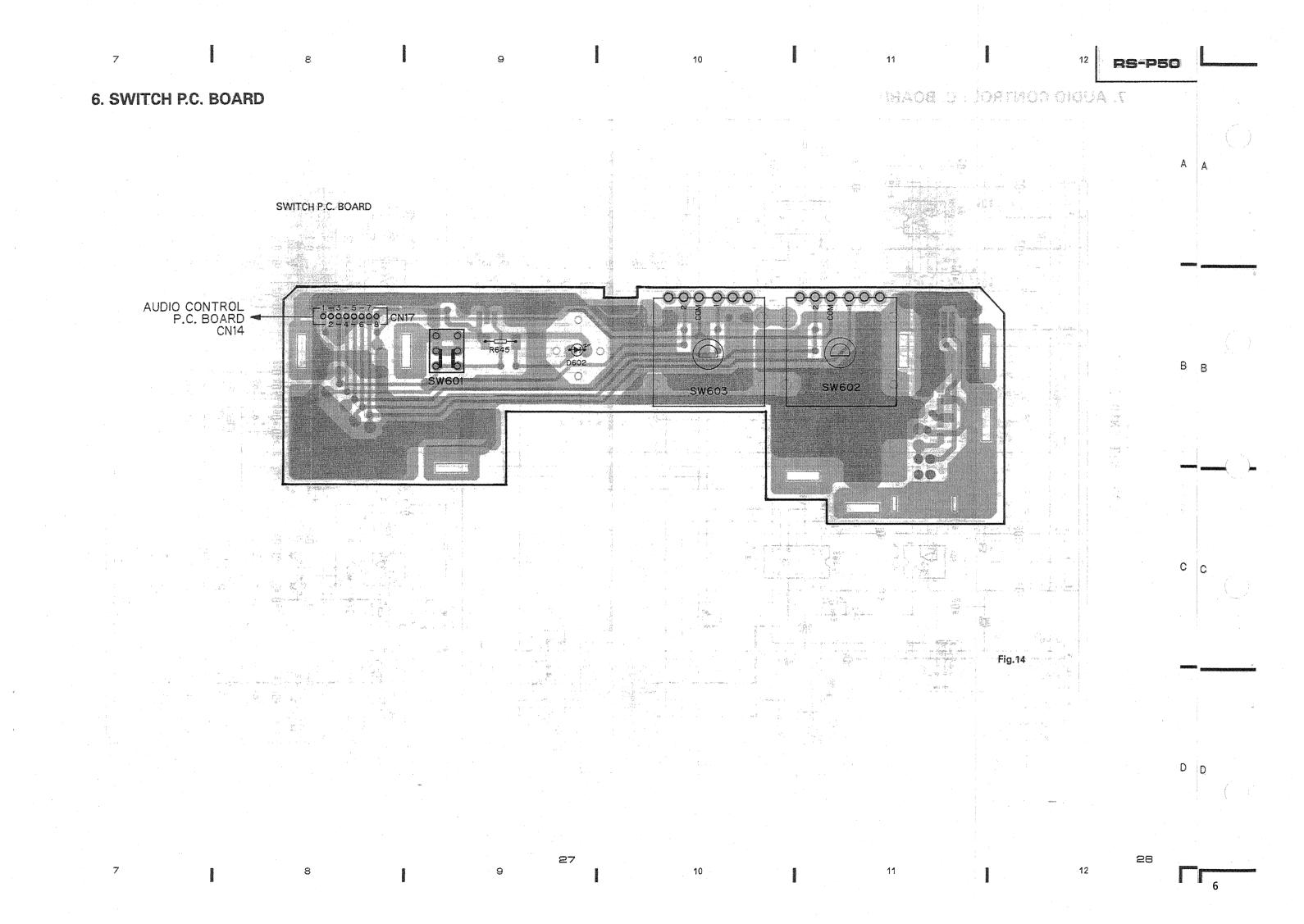
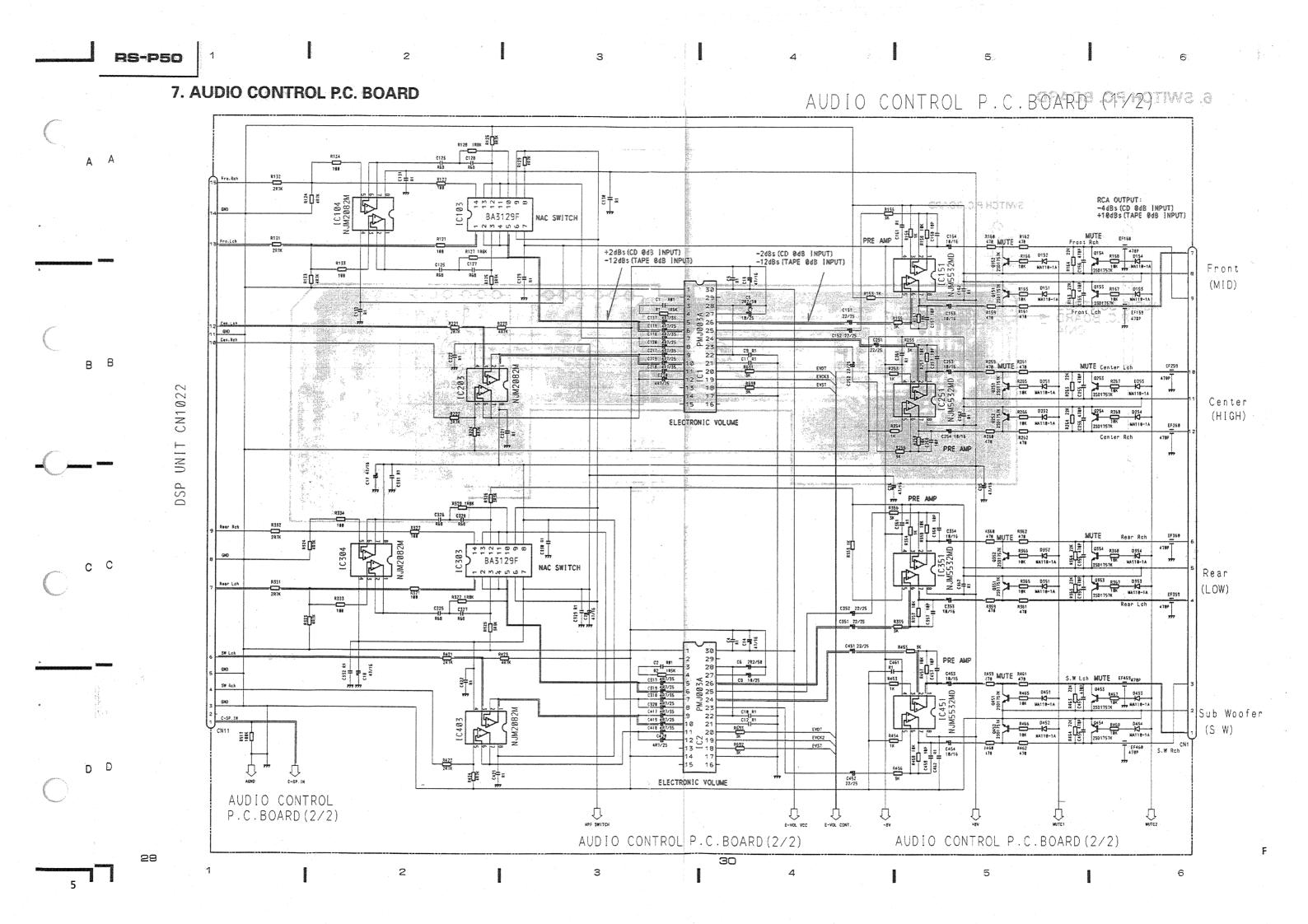


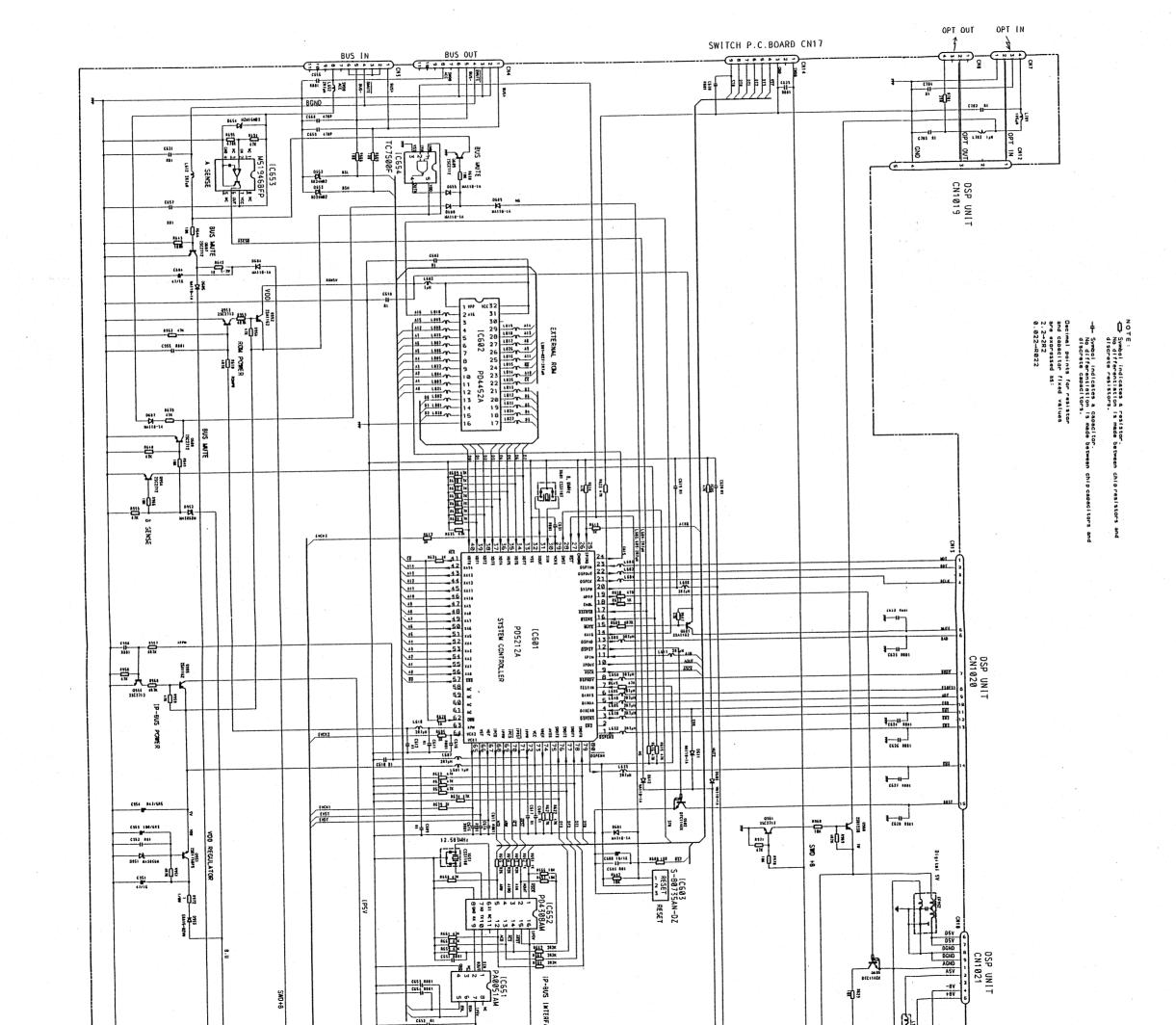
Fig.13

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AUDIO CONTROL P.C. BOARD 5. SCHEMATIC CIRCUIT DIAGRAM Q454 iC451 Q251 Q154 iC351 Q554 Q451 Q452 Q153 Q453 IC1 IC151 Q1'Q2 Q152 Q352 Q254 Q353 IC551 IC502 IC2 IC251 IC951 Q555 Q151 Q252 Q351 Q253 Q354 Q358 Q608 (C653 | C654 Q803 Q801 Q954 Q609 Q607 Q960 Q957 Q961 Q802 Q959 Q601 Q602 → DSP UNIT CN1022 DSP UNIT CN1019 -DSP UNIT CN1020 -SWITCH P.C. BOARD CN17 FRONT CENTER REAR SUB WOOFER OUTPUT O B REMOTE OUT BACK UP DSP UNIT CN1021 Fig.16







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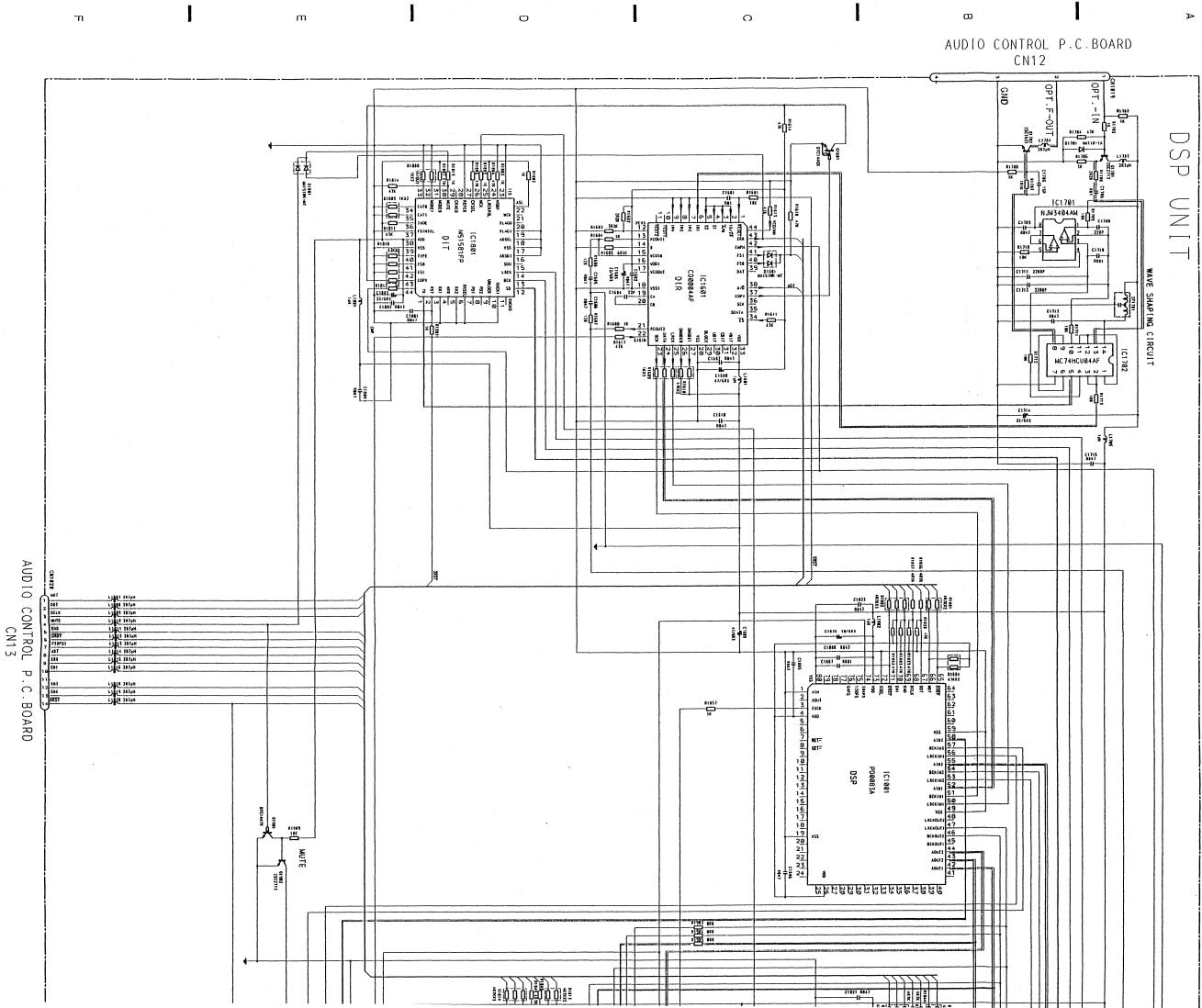
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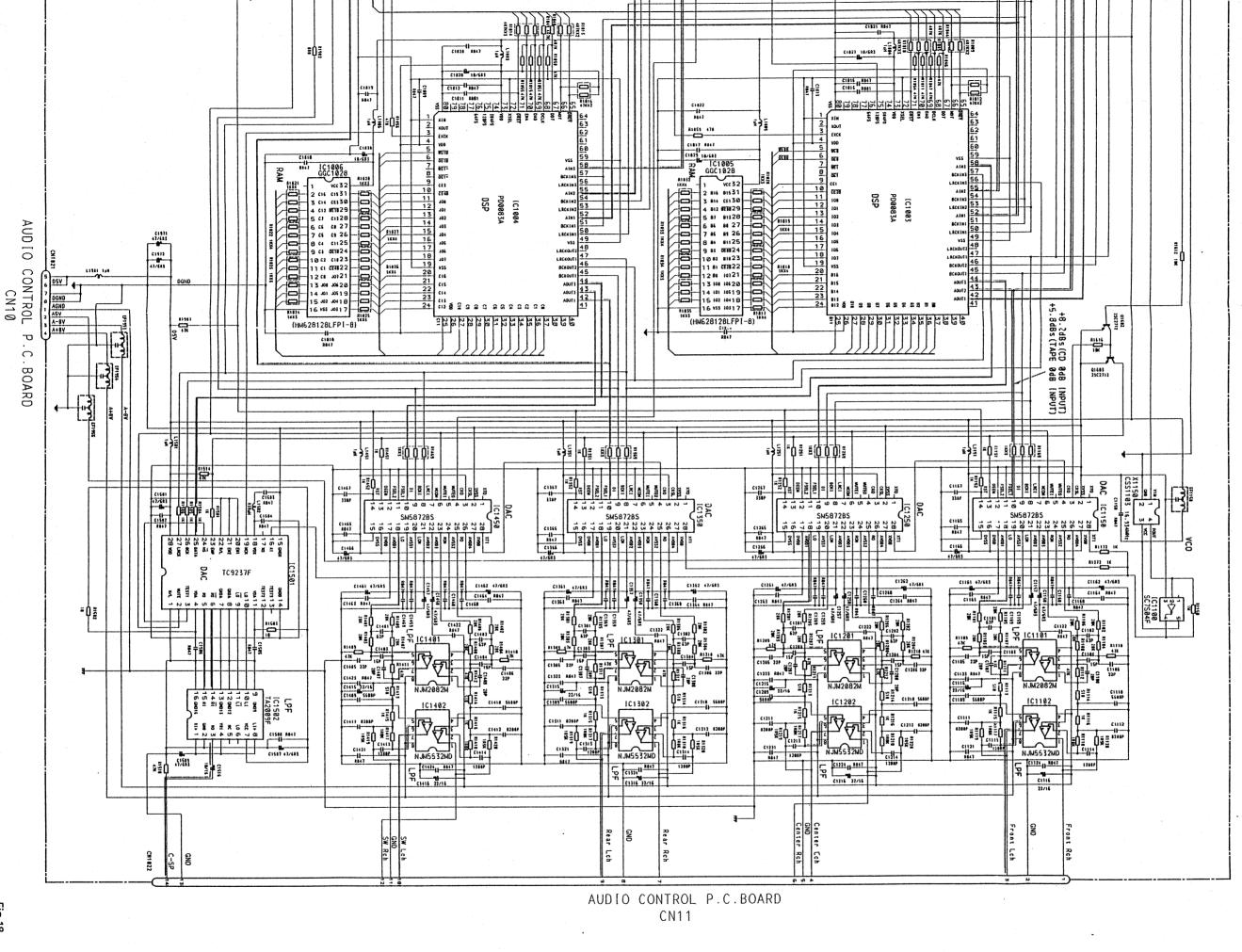
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RS-P50



 $\omega$ 



RS=P50

DSP UNIT

WHY CHOOLSKE !

Q1701 IC1102 IC1150 IC1101 IC1801 Q1902 Q1702 Q1901 Q160

Q1702 Q1902 Q1603 IC1201 - IC1701 IC1702 Q1601 IC1100 IC1601 IC1250 IC1202

IC1301 1001 IC1350 IC1302

3

IC1401 IC1402 IC1004 IC1003 IC1450

IC1501 IC1502 IC1006 IC1005

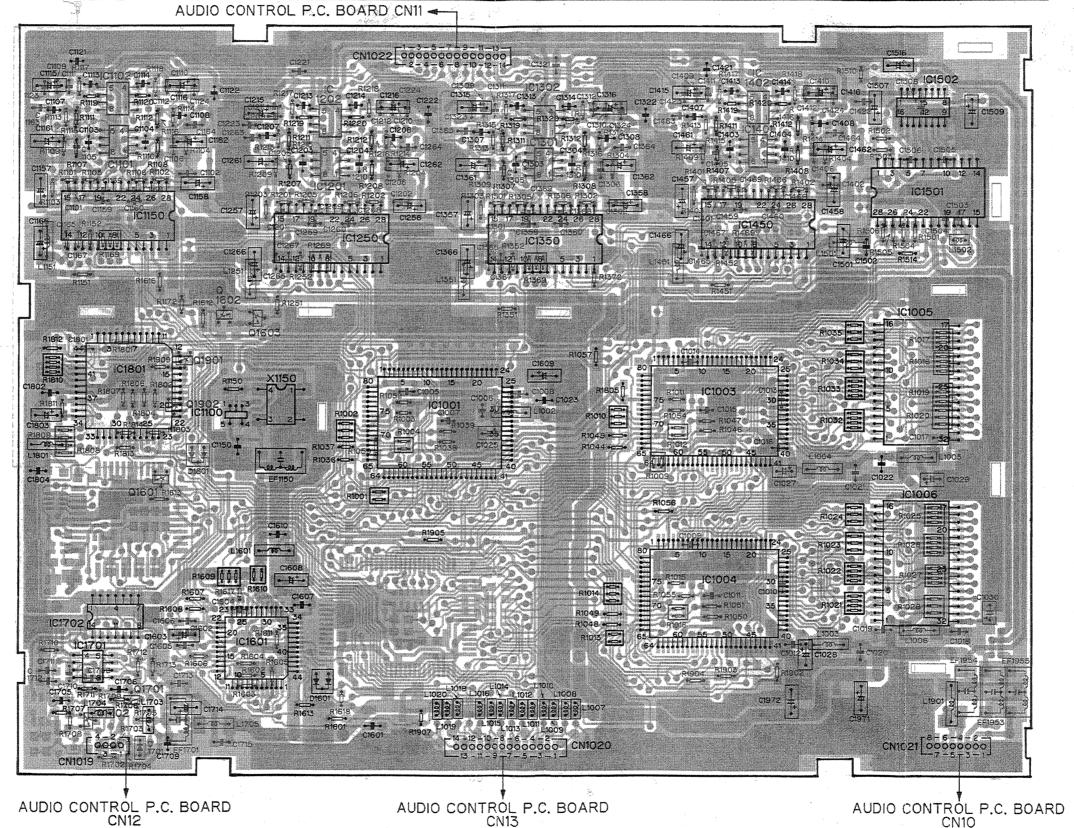


Fig.19

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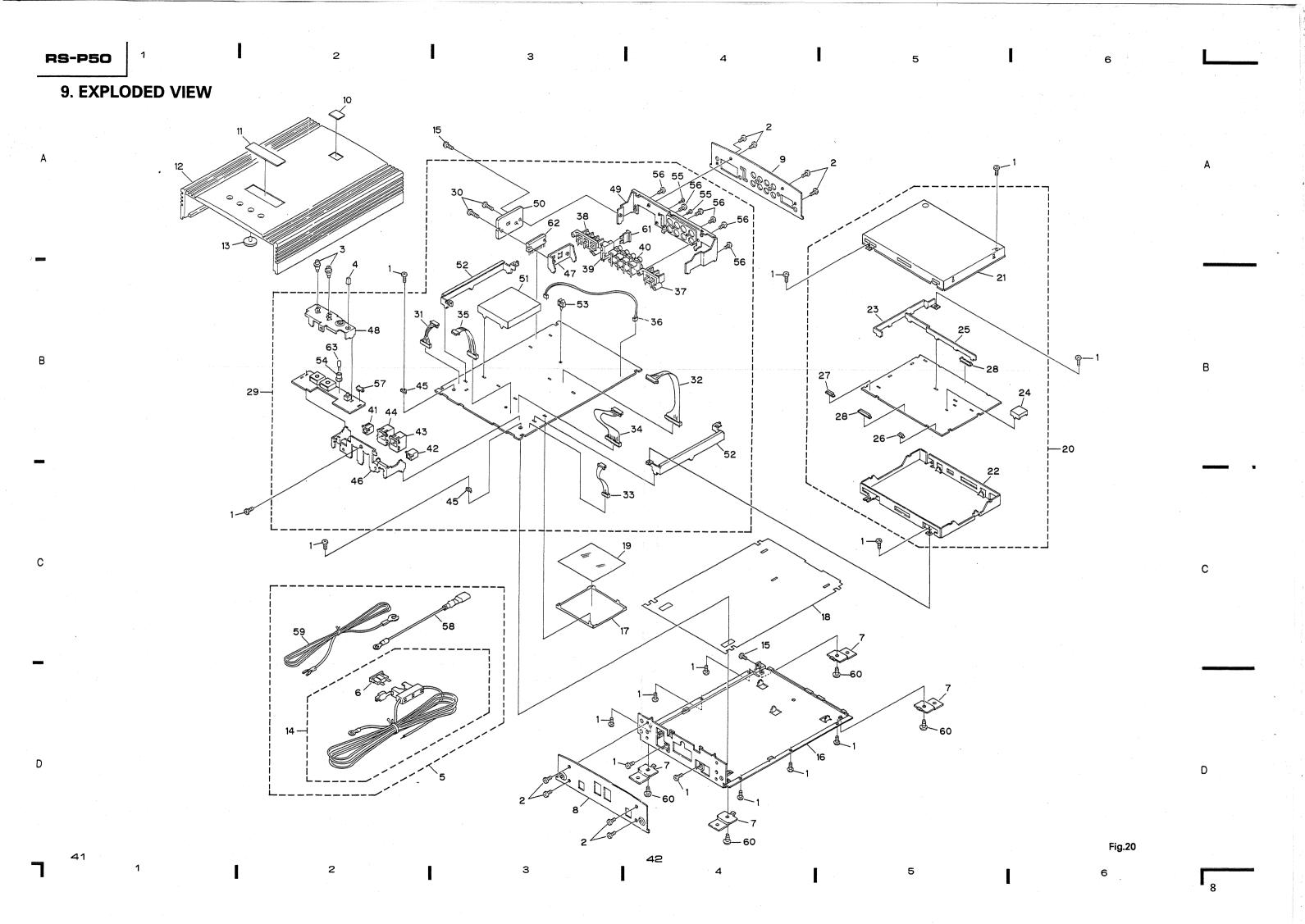
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## NOTES:

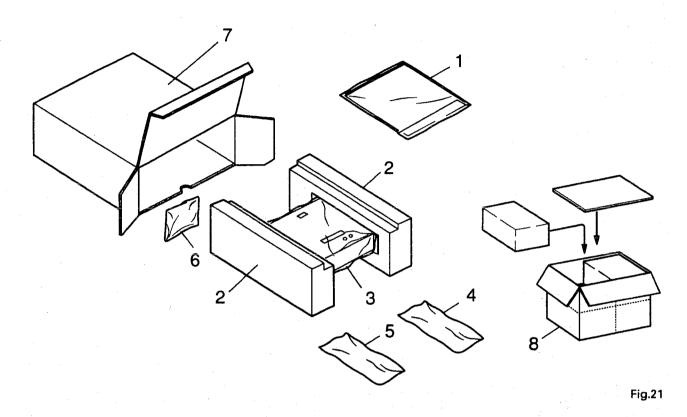
- Parts marked by "# "are generally unavailable because they are not in our Master Spare Parts List.

  Parts marked by "@"are not always kept in stock. Their delivery time may be longer than usual or they may be unavailable.

## Parts List

Mark	No.	Description	Part No.	. 1	Mark	No.	Description	Part No.
	1	Screw	BMZ30P050FCU	_		34	Cord(CN13)	CDE3969
		Screw	BMZ30P050FZK			35	Cord(CN14)	CDE3970
		Knob	CAA1316			36	Connector	CDE4164
	_	Button	CAC3566			37	Terminal(CN5)	CKE1005
	5	Cord Assy	CDE3955			38	Terminal(CN6)	CKE1032
	6	Fuse	CEK1001				Auto Fuse Holder(CN2)	CKR1011
*	7	Bracket	CNC4763				Pin Jack(CN1)	CKS1840
	8	Panel	CNB1702				Connector(CN8)	CKS1940
	9	Panel	CNB1703			42	Connector(CN7)	CKS2601
	10	Badge	CAH1399			43	Connector(CN3)	CKS2602
	11	Badge(UC)	CAH1427				Connector(CN4)	CKS2603
		Badge(EW)	CAH1426			45	Holder	CNC2218
	12	Heat Sink(UC)	CNR1320			46	Holder	CNC4753
		Heat Sink(EW)	CNR1319			47	Holder	CNC4755
	13	Lens	CNS2053			48	Holder	CNC4756
	14	Cord	CDE3949			49	Holder	CNC4757
	15	Screw	BMZ30P060FCU			50	Heat Sink	CNC4758
	16	Chassis	CNA1513			51	Shield	CNC4761
		Shield	CNC5042			52	Holder	CNC4910
	18	Insulator	CNM3611			53	Clamper	CNV1443
	19	Insulator	CNM3806				Holder	CNV3459
	20	DSP Unit	CWE1290				Screw	PPZ20P060FZK
	21	Case	CNC4759				Screw	PPZ30P080FZK
	22	Shield	CNC4760			_	Connector(CN17)	CKS2197
	23	Holder	CNC4909			58	Cord	CDE3960
	24	Shield	CNC5033				Cord	CDE3951
	25	Holder	CNC5038				Screw	BMZ40P050FMC
	26	Connector(CN1019)	CKS2193				Fuse(FU951)	CEK1001
	27	Connector(CN1021)	CKS2197			62	IC(IC551)	TA8225L
		Connector(CN1020,1022)	CKS2203			63	Diode(D602)	PR3432S
	29	Audio Control Unit	CWM3671					
		Screw	BMZ30P120FCU					
		Cord(CN10)	CDE3966					
		Cord(CN11)	CDE3967					
		Cord(CN12)	CDE3968					

## **10. PACKING METHOD**



## Parts List

Mark	No.	Description	Part No.	Mark	No.	D
	1-1	Owner's Manual(UC)	CRD1706		4-4	S
		Owner's Manual(EW)	CRD1659	*	4-5	P
		Owner's Manual(EW)	CRD1660		5	Α
*	1-2	Warranty Card(UC)	CRY1053	*	5-1	В
*		Card(EW)	CRY-062	*	5-2	Ρ
	1-3	Cover(18mm)	CNS2227		6	С
	1-4	Cover(25mm)	CNS2726		7	С
	1-5	Driver	CNV3579			С
*	1-6	Polyethylene Bag	E36-634		8	C
	2	Protector(×2)	CHP1578			С
	3	Cover	CEG1082	<u>1-1 O</u>	wner'	's l
	4	Screw Assy	CEA1848	Part	No.	
	4-1		BMZ40P050FMC	CRD	1706	
	4-2	Screw(×4)	BYC40P120FZK		1659	-
	4-3	Screw	CBA1241	0	1000	
				CBC	1660	

Mark	No.	Description	Part No.
	4-4	Shaft	CLP1100
*	4-5	Polyethylene Bag	E36-613
	5	Accessory Assy	CEA1849
*	5-1	Bracket(×4)	CNC4763
*	5-2	Polyethylene Bag	CEG-020
	6	Cord Assy	CDE3955
	7	Carton(UC)	CHG2330
		Carton(EW)	CHG2331
	8	Contain Box(UC)	CHL2330
		Contain Box(EW)	CHL2331

Manual

Part No.	Model	Language
CRD1706	RS-P50/UC	English,French
CRD1659	RS-P50/EW	English,Italian,French, German
CRD1660	RS-P50/EW	Spanish,Swedish, Dutch

## 11. ELECTRICAL PARTS LIST NOTE:

- Parts whose parts numbers are omitted are subject to being not supplied.
- The part numbers shown below indicate chip components.

Chip Resistor

RS1/OSOOOJ,RS1/OOSOOOJ

Chip Capacitor (except for CQS.....)

CKS....., CCS....., CSZS.....

=====Circuit Symbol & No. Pa	art Name=====	Part No.	====Circuit Symbol & No. Part Name=====	Part No.
Unit Number : CWE1290			R 1107 1108 1207 1208 1307 1308 1407 1408	RN 1/10SE203
Jnit Name : DSP Unit			R 1109 1110 1209 1210 1409 1410	RN1/10SE473
5111. 11. 11. 15. 15. 15. 15. 15. 15. 15			R 1111 1112 1211 1212 1311 1312 1411 1412	RN1/10SE473
MISCELLANEOUS			R 1113 1114 1213 1214 1313 1314 1413 1414	RN 1/10SE511
MISCELLMIAEOOS			R 1115 1116 1215 1216 1315 1316 1415 1416	
C 4004 4000 4004		DD0000A	N 11151110 121512101315 13101415 1410	RN 1/10SE 102
C 1001 1003 1004		PD0083A		
C 1005 1006 (HM6	28128LFPI-8)	GGF1028	R 1117 1118 1217 1218 1317 1318 1417 1418	RN1/10SE182
C 1100		SC7S04F	R 1119 1120 1219 1220 1319 1320 1419 1420	RN 1/10SE 152
C 1101 1201 1301		NJM2082M	R 1150	RS1/10S105J
C 1102 1202 1302 1402		NJM5532MD	R 1151 1152 1251 1252 1351 1352 1372 1451 1452 1501	RS1/10S102J
			R 1172	RS1/10S102J
C 1150 1250 1350 1450		SM5872BS		
C 1401		NJM2082M	R 1205 1206 1301 1302 1303 1304 1305 1306 1401 1402	DN1/100E202
		TC9237F	R 1309 1310	
C 1501				RN 1/10SE473
C 1502		TA2009F	R 1403 1404 1405 1406	RN 1/10SE203
C 1601		CD0004AF	R 1502 1503	RS1/10S100J
			R 1504 1505 1506 1702 1703 1705 1708 1801 1802	RS1/10S102J
C 1701		NJM3404AM		
C 1702		MC74HCU04F	R 1510	RS1/10S473J
C 1801		M51581FP	R 1514 1613	RS1/10S473J
1 1601		DTC144EK	R 1601 1711	RS1/10S103J
		2SC2712	R 1602 1603	•
1 1602 1603 1701 1902		2002/12		RS1/10S222J
			R 1604 1803 1813	RS1/10S102J
1 1702		2SC1621		
1901		DTC144TK	R 1605	RS1/10S622J
1601		MA151WK-MT	R 1606	RS1/10S121J
1701		MA110-1A	R 1607	RS1/10S121J
1801		MA151WK-MT	R 1608 1805 1812	RS1/10S102J
			R 1610	RA2CQ473J
1002 1003 1004 1005 Induc	tor :	LCTA1R0K4532	11 1010	1172004730
1002 1003 1004 1005 made			D 1610 1616 1700 1710 1710 1000	DC4/40C4001
		LCTA 1R0K4532	R 1612 1616 1709 1710 1712 1909	RS1/10S103J
. 1007 1008 1009 1010 Induc		LCTB2R7K2125	R 1614 1618	RS1/10S473J
. 1011 1012 1013 1014  induc	tor	LCTB2R7K2125	R 1617	RS1/10S471J
_ 1015 1016 Induc	tor	LCTB2R7K2125	R 1706	RS1/10S222J
			R 1707	RS1/10S112J
_ 1018 1019 1020 Induc	tor	LCTB2R7K2125		
1451 1501 1705 Induc		LCTA1R0K4532	R 1713	RS1/10S101J
_ 1502 Induc		LCTBR39K2125	R 1808 1809	RA2CQ102J
. 1601 1801 Induc		LCTA1R0K4532	R 1810	RA4C473J
		LCTA2R7J3225	R 1902 1903 1904	
_ 1703 1704 Induc	aor .	EC 1A2N/33225		RS1/10S0R0J
			R 1905	RS1/10S0R0J
. 1901 Induc	ctor	CTF1250		
( 1150 VCO		CSS1103	R 1907	RS1/10S473J
F1150 EMI F	ilter	CCG1030		
F1701 1953 1954 1955 EMI F	ilter	CCG1030	CAPACITORS	
FEICTORE			C 1005 1006 1008 1009 1010 1012 1013 1014 1016 1018	CKCOADATOR
ESISTORS				
			C 1007 1011 1015 1710	CKSQYB102
1001 1013		RA2CQ472J	C 1017	CKSQYB473
1 1002 1010 1014		RA3C472J	C 1019 1020 1021 1602 1605 1606	CKSQYB473I
1003 1011 1015 1704 1804 1	1806 1807 1811 1814	RS1/10S473J	C 1022 1023 1607 1610	CKSQYB473I
1004 1012 1016		RA2CQ473J		
1009		RA2CQ472J	C 1025 1027 1029 1030	CSZSR100M
	•		C 1028	CSZSR100M
1017 1010 1010 1025 1025 1	1027	RA4C102J	C 1101 1102 1201 1202 1301 1302 1401 1402	
1017 1018 1019 1025 1026 1				CCSQCH620
1 1020 1028 1169 1269 1369 1	1409	RA3C102J	C 1103 1104 1203 1204 1303 1304 1403 1404 1705	CCSQCH150
1021 1022 1032 1033		RA4C102J	C 1105 1106 1205 1206 1305 1306 1405 1406 1604	CCSQCH220
1023 1024 1034 1035 1609		RA3C102J		
1036 1037 1044 1045 1048 1	1049	RS1/10S472J	C 1107 1108 1207 1208 1307 1308 1407 1408	CCSQCH200
			C 1109 1110 1209 1210 1309 1310 1409 1410	CFHSQ562G
1038 1039 1046 1047 1050 1	1051 1052 1054 1055 1611	RS1/10S473J	C 1111 1112 1211 1212 1311 1312 1411 1412	CFHSQ822G
1036 1039 1046 1047 1030 1	1002 1007 1000 1011	RS1/10S471J	C 1113 1114 1213 1214 1313 1314 1413 1414	CFHSQ122G
			C 1115 1116 1215 1216 1315 1316 1415 1416	
1057		RS1/10S102J	C 1110 1110 1210 1210 1310 1310 1415 1410	CEV220M16
1059		RS1/10S471J RN1/10SE203D		
1101 1102 1103 1104 1105 1				

=====Circuit Symbol & No. Part Name=====	Part No.	=====Circuit Symbol & No. Part Name=====	Part No.
C 1121 1122 1222 1322 1502 1709 1802 1804 C 1123 1124 1159 1160 1163 1164 1165 1221 1223 C 1150 C 1157 1158 1161 1162 1166 1257 1258 1261 1262 1266 C 1167 1267 1367 1467	CKSQYB473K16 CKSQYB473K16 CKSQYB473K16 CEV470M6R3 CCSQSL331J50	D 554 D 555 D 602 D 607 608 609 D 611 612	MA3056M ERA15-02VH PR3432S MA110-1A MA110-1A
C 1168 1169 1268 1269 1368 1369 1468 1469 C 1224 1259 1260 1263 1264 1265 1321 1323 1324 1359 C 1357 1358 1361 1362 1366 1457 1458 1461 C 1360 1363 1364 1365 1423 1424 1459 1460 1463 C 1421	CKSQYB473K16 CKSQYB473K16 CEV470M6R3 CKSQYB473K16 CKSQYB473K16	D 652 653 D 654 D 655 D 801 803 804 D 802	RD24MB2 HZM16NB3 MA110-1A MA110-1A MA3075H
C 1422 C 1462 1466 1501 1507 1509 1608 1609 1971 1972 C 1464 1465 1503 1504 1505 1506 1508 1713 1715 1801 C 1516 C 1601 1706	CKSQYB473K16 CEV470M6R3 CKSQYB473K16 CEV100M16 CKSQYB103K25	D 951 954 D 952 D 953 L 50 51 52 Choke Coil L 66 67 86 87 Coil	MA3056M MA3068M ERA15-02VH CTH1109 CTH1110
C 1603 1714 1803 C 1708 C 1711 1712	CEV220M6R3 CKSQYB221K50 CKSQYB222K50	L 601 602 651 702 Inductor L 603 604 605 606 Inductor L 607 608 Inductor L 610 Inductor L 611 Inductor	LCTA1R0J3225 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125
Audio Control Unit Consists of Switch P.C.Board Audio Control P.C.Board		L 612 Inductor L 630 632 633 648 Inductor L 650 Inductor L 652 Inductor L 682 683 Inductor	LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125
Unit Number : CWM3671 Unit Name : Audio Control Unit MISCELLANEOUS		L 701 Inductor L 801 809 810 Inductor L 802 803 804 805 Inductor L 806 807 Inductor	LCTA1R5J3225 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125
IC 1 2 IC 50 IC 66 IC 103 303 IC 104 203 304 403	PMJ003A TL5001PS TL1451ANS BA3129F NJM2082M	L 808 811 812 813 Inductor  L 814 815 816 817 Inductor  L 818 819 Inductor  L 820 Inductor	LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125 LCTB2R7K2125
IC 151 251 351 451 IC 501 IC 502 IC 551	NJM5532MD UPC4570G PMJ003A TA8225L	L 821 822 823 824 Inductor L 825 826 827 Inductor L 951 Choke Coil L 952 953 954 Inductor	LCTB2R7K2125 LCTB2R7K2125 CTH1103 CTF1200
IC 601 IC 602 IC 603 IC 651	PD5212A PD4452A S-80735AN-DZ PA0051AM	L 955 Coil T 66 86 Transformer RY 551 Relay X 601 Ceramic Resonator	CTF1135 CTT1030 CSR1010
IC 652 IC 653 IC 654 IC 951	PD4308AM M51946BFP TC7S00F PML001A	X 651 Ceramic Resonator SW601 Switch SW602 603 Switch FU 951 Fuse 4A	CSS1108 CSG-249 CSD1021 CEK1001
Q 1 2 552 Q 50 66 86 Q 51 67 87 Q 52 53 68 88	DTA114EK 2SA1797 2SC2812 2SA1179	EF 159 160 259 260 EMI Filter EF 359 360 459 460 EMI Filter EF 951 EMI Filter EF 952 EMI Filter	CCG1050 CCG1050 CCG1006 CCG1025
Q 54 Q 151 152 153 154 251 252 253 254 351 352 Q 353 354 451 452 453 454 553 Q 554	DTC124EK 2SD1757K 2SD1757K 2SC2712	RESISTORS  R 1 R 2 R 3 4	RS1/10S152J RS1/10S152J RS1/10S102J
Q 555 803 Q 601 952 Q 602 606 Q 607 608 609 953 954 956 958 Q 801 957 960	2SC3113 2SA1162 DTC114EK 2SC2712 2SB1238	R 50 66 86 R 51 63 R 52 68 88 R 53 69 89 121 122 133 134 321 322 333	RS1/10S122J RS1/10S223J RS1/4S681J
Q 802 961 Q 951 Q 955	2SC2712 2SD1760F5 2SA1162	R 54 73 74 80 R 55 R 56	RN1/10SE103D RN1/10SE302D RS1/10S473J RN1/10SE223D
Q 959 D 50 D 66 86 D 151 152 153 154 251 252 253 254 351 352		R 57 R 58 75 R 59 R 60 61 R 62 606 635 636 639 640 647 684 685	RS1/10S474J RS1/10S333J RS1/10S221J RS1/10S473J
D 353 354 451 452 453 454 601 603 604 605 D 501 D 551 552 957 958 959			

==:			Symt				Name	====	= 	Part No.	==	===C	ircuit	Sym	bol &		Part	Vame	====	=	Part No.
R R R	64 67 70						367	368	467	RN1/10SE182D RS1/10S103J RN1/10SE303D	R	656					••••				RS1/10S222J RS1/10S103J RS1/10S302J
R R	71 76	30								RN1/10SE622D RN1/10SE912D		686	687		689	954	955	958	959	961	RS1/10S473J RS1/10S302J
R R	77 78									RN1/10SE153D RN1/10SE273D	R R		693								RS1/10S302J RS1/10S473J
	81									RN1/10SE2/3D		695									RS1/10S474J
R R	82									RN1/10SE392D		701									RS1/10S391J
R	91									RN1/10SE432D			822	823	966	968					RS1/10S103J
R	92									RN1/10SE222D	R	824	963	971							RS1/10S473J
R	123	124	323	324	610					RS1/10S472J	R	825									RS1/2S102J
R			325							RS1/10S332J		826									RS1/10S102J
R		128	327	328						RS1/10S182J			952								RS1/10S473J
R	129	223	224							RS1/10S472J	R	951									RS1/10S472J
R		132			331		421	422		RS1/10S272J		957	000								RS1/10S472J
R	155	154	253	254	453	454				RS1/10S102J RS1/10S302J	R	965 967	303								RS1/10S472J RS1/10S102J
R R		255	256	355	256	AEE	456			RS1/10S302J		970									RS1/10S1023
R	157	158		258		400	430			RS1/10S103J		972									RS1/8S1R0J
R		160		162			359	360		RS1/10S471J	R	973									RS1/10S4R7J
R		164		364		464				RS1/10S223J											
R		166	265	266	365	366	465	466		RS1/10S103J	CA	PACI	TORS	5							
R	260									RS1/10S471J	С		962								CKSQYB103K25
R	263	264								RS1/10S223J	·C		603	952	969						CKSQYB103K25
R	334	600	665	666						RS1/10S101J	č	3		9		11	12	704	958		CKSQYB104K25
R	353	000	000	000						RS1/10S102J	č	5	6	·		• • •			-		CEAS2R2M50
R	354									RS1/10S102J	č	7	_								CEAS100M25
R		457	458							RS1/10S103J	•	•	•								
R	361	362	459	460	461	462	618			RS1/10S471J	C	13 15	14 16	515 17	951 18		960	977			CEA470M16LL CEA470M16LL
R	423	424	609	953	960	962				RS1/10S472J	. C	50	52	56	58	39µ	F/25V				CCH1162
₹	511									RS1/10S103J	С	54									CKSQYB331K50
?	512									RS1/10S152J	. C	55	61	70	71	80	81	90	91		CKSQYB102K50
₹	513									RS1/10S222J	_										
3	514									RS1/10S563J	C	59	75			653	654				CKSQYB102K50
										DC1/10C1001	C	60		77	97						CKSQYB222K50 CKSQYB104K25
}	516									RS1/10S102J RS1/10S104J	c	62 66	68	72	73	30	F/25V				CCH1162
₹ ₹	517 551	EGE								RS1/10S122J	č	74		88	/3		F/25V				CCH1162
ì	552	500								RS1/10S103J	·	′-	00	~		σσμι	. , 25 1				COTTIOE
i	553									RS1/10S102J	С	78									CCSQCH221J50
•											С	82									CEHAS010M50
1	554									RS1/10S153J	С	92		94			F/25V				CCH1162
1	555									RS1/10S473J	C		118				318				CEA4R7M35LL
l	556									RS1/10S473J	С	119	120	219	220	319	320	419	420		CEWAR4R7M25
?	557									RS1/10S620J	_	400	100	407	100						CETYAGGALEO
₹	558									RS1/10S620J	C		126 130	127	128	603	652	702	702		CFTXA684J50 CKSQYB104K25
₹	559									RS1/10S302J				161		002	052	702	/03		CKSQYB104K16
R										RS1/10S302J						351	352	451	452		CEZA220M25
R		562	566	567						RS1/8S2R2J							354				CEZA100M16
ì			641							RS1/10S103J											
₹	572									RS1/10S472J	С	155	156	255	355	356	455	456			CCSQSL471J50
														357	358						CCSQCH100D50
3	573									RS1/8S332J			222								CKSQYB104K16
	574									RS1/10S473J		256									CCSQSL471J50
	605									RS1/10S302J	C	257	258	457	458						CCSQCH100D50
	607	629								RS1/10S103J	_	201	202	201	401						CKSQYB104K16
H	612									RS1/10S302J				361 327							CFTXA684J50
	612	620	624	651	852					RS1/10S102J				362							CKSQYB104K16
							625	626	627	RS1/10S473J				462							CKSQYB104K16
	617	0.0	0.0					-		RS1/10S102J			511								CEA010M50NPLL
R	619									RS1/10S102J											
R		658	659	660	661					RS1/10S473J	C	512									CEWAR100M16
											С	513									CKSQYB103K25
R	642									RS1/2S102J				522	523	524	525	526	530		CKSQYB104K16
R	643									RS1/10S104J		516									
R										RS1/2S681J	С	517									CKSQYB473K16
R	649									RS1/10S473J											
R	653	654								RS1/10S222J											

## RS-P50

==	===C	ircuit 	Symi	001 &	No. Part Name=====	Part No.
С	519					CEA470M16LL
С	520					CKSQYB103K2
С	528					CEA2R2M50LL
Č	529					CEA100M16LL
č	551				•	CEZA010M50
С	552					CKSQYB103K2
С	553					CEA010M50LL
C	554					CEAS470M16
С	555	556				CEAS101M16
С	557	558			470μF/16V	CCH-114
С	559	560				CFTXA473J50
Ċ	562					CEZA010M50
С	601					CKSYB104K16
С	604					CEA470M16LL
С	608					CEA100M16LL
С	609	617	618	656		CKSQYB104K
С	610	611	612	619	620	CKSQYB104K1
С	613	614	631			CKSQYB102K
С	615	616				CKSQYB102K
С	621					CKSQYB103K
С	622	623	624	626	627 628 629 630	CKSQYB102K5
С	655					CKSQYB102K
С	657					CKSQYB103K2
С	659	660				CCSQCH471JE
С	801					CEA470M25LL
С	953					CEA101M6R3L
С	954	•			0.47μF/5.5V	CCL1016
С	955					CKSQYB102K
С	956					CKSQYB102K
С	961					CKSQYB473K
С	967				4700μF/16V	CCH1068
С	968					CKSQYB104K2
С	976					CKSQYB102K
Ç	978					CEA470M16LL
Mi	iscella	neou	ıs Par	ts Lis	<b>t</b>	
FU	J 1				Fuse 4A	CEK 1001

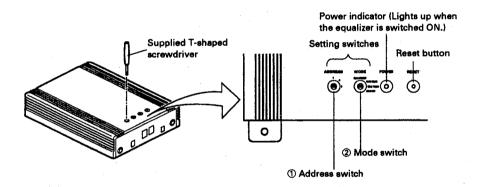
## 12. OPERATIONS AND CONNECTION

## Setting of this unit

This unit has two setting switches: address and mode. Set these switches according to your system. Incorrect setting will result in improper operation of the ODR System.

## Switching the setting

- 1. Change the setting of the switches using the T-shaped screwdriver supplied with the equalizer.
  - > Keep the supplied T-shaped screwdriver in a safe place.



## 2. Press the Reset button.

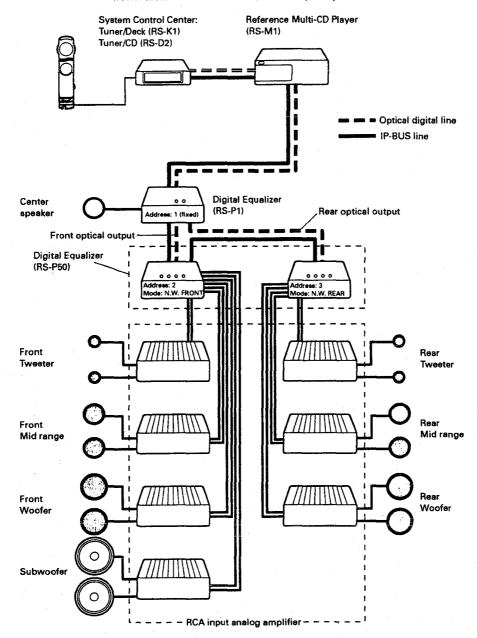
Always press the Reset button after changing a setting switch (see "Reset Button"). Otherwise the new setting will not be registered.

## **Setting Example**

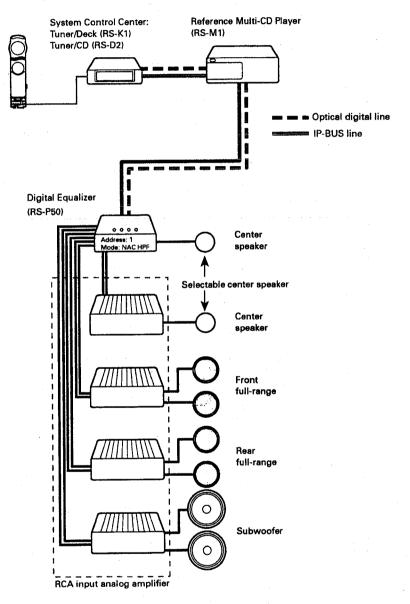
> Read the manual of the ODR System Control Center for specific setting examples.

## Using the RS-P50 for digital network (Example of maximum system connection )

- > Connect the center speaker to the center speaker output of the digital equalizer which is assigned as Address 1. No sound will be output when the speaker is connected to the center speaker output of the digital equalizer assigned as either Address 2 or 3.
- > To ensure better sound quality, connect the subwoofer to the front OUT. (Although the subwoofer should be connected to the front OUT, it may be installed to either front or rear.)



## Using the RS-P50 for NAC



## 1 Address setting

Multiple audio units\* such as the RS-P50 Equalizer and Digital Amplifiers can be connected to the ODR System. Therefore, each audio unit must be assigned an address as identification number (1 to 8).

Set the address according to the following rules:

- · Set a unique address to each audio unit.
- The first audio unit must be assigned to Address 1.
- If the Digital Equalizer RS-P1 is included among the audio units, it must be assigned as Address 1. Therefore, assign either Addresses 2 or 3 to the Digital Equalizer RS-P50.
- > The RS-P1 is fixed to Address 1, and this setting cannot be changed.
- If the Digital Equalizer RS-P1 is not used, assign Address 1 to one of the RS-P50.

## 2 Mode setting

Set the mode of the RS-P50 according to your system.

## N.W. FRONT (network/front) mode N.W. REAR (network/rear) mode

Set to this mode when using the unit as a digital network. To output front audio from RCA OUTPUT, set to N.W. FRONT. To output rear audio from RCA OUTPUT, set to N.W. REAR.

RCA OUTPUT: Outputs high-range, mid-range, low-range and subwoofer audio.

Optical OUTPUT (Address1): Outputs signals with reversed channels from front/rear of RCA OUTPUT.

(Address 2 or 3): Outputs signals as they are from Optical INPUT.

### NAC THRU mode NAC HPF (high-pass filter) mode

Set to this mode when using the unit as NAC (Natural Acoustic Control). Setting is possible only when the unit is assigned as Address 1.

RCA OUTPUT: Outputs center speaker, front, rear and subwoofer audio.

Optical OUTPUT: Outputs front signals.

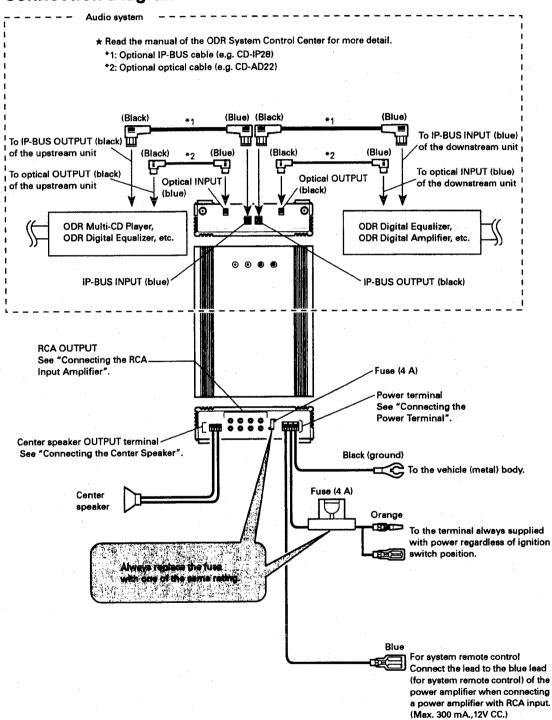
When the unit is set to NAC THRU, front/rear output will be full range. When the unit is set to NAC HPF, a high-pass filter of 100 Hz will be applied only to front/rear output. Set the unit to NAC HPF when using the subwoofer.

### \*Audio units

By definition, audio units are Digital Amplifiers, Digital Equalizer and other audio equipment for the ODR System including the following:

- Dual 1/3 Octave Digital Equalizer [RS-P1]
- Universal Digital Preamp/Equalizer [RS-P50]
- Digital "Pure Class A" Integrated Amplifier [RS-A1]
- Digital "Class A" Integrated Amplifier [RS-A2]

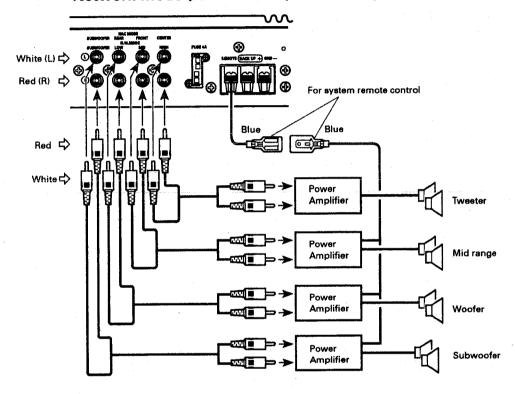
## **Connection Diagram**



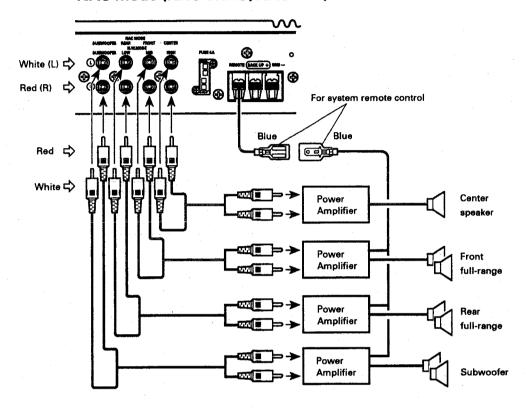
## **Connecting the RCA Input Amplifier**

Connect the RCA Input Power Amplifier according to the purpose of the speaker to be connected to the system.

## Network mode (N.W. FRONT/N.W. REAR)

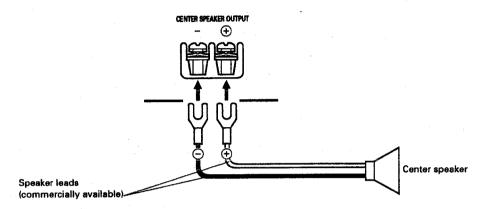


## NAC mode (NAC THRU/NAC HPF)



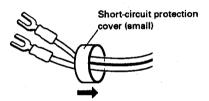
## **Connecting the Center Speaker**

When the equalizer is assigned as Address 1, the center speaker can be connected to the center speaker output terminal.

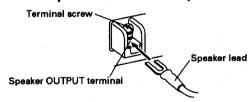


## Connecting the speaker terminal

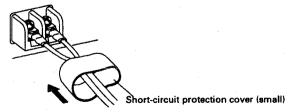
1. Put the short-circuit protection cover (small) around the speaker leads.



- Be sure to use this cover to prevent short-circuit.
- 2. Connect the speaker leads to the speaker terminal.



· Fasten the speaker leads firmly with terminal screws.



3. Cover the entire terminal with the short-circuit protection cover (small).